**Contact Address:**
R/O Agon, Firozpur Jhirka,
Gurgaon- 122104 (HR.)
**Telephone:**
+91-9468070111(Mobile)
+91-9253349881(Mobile)

**Email:**
**bhoop.saini@gmail.com**

**Personal Details**:
Name: Bhoop Singh Saini
Father's name: Sh. Kundan Lal
Date of Birth : Dec 21,1984
Sex : Male
Nationality : Indian
Marital Status: Single
Languages : English, Hindi
Known

**My Strengths:**

- Self Confidence
- Positive approach
- Adaptable to changes
- Ability to learn new Technologies

**Area of interest:**

- VLSI/ Microelectronics
  - √ Digital Design using CMOS
  - √ Analog IC Design
  - √ IC Fabrication
  - √ Digital design using VHDL
- Communication & Microprocessors
  - √ Advanced Microprocessors
  - √ Network Theory
  - √ Communication Systems
- Embedded System Design

√ PIC microcontrollers

## OBJECTIVE

To utilize my knowledge and abilities in a competitive environment and a part of team that dynamically works towards the growth of organization and gain satisfaction there off.

## EDUCATIONAL QUALIFICATION

***PhD in Electronics (sponsored by World Bank TEQIP )** pursuing from Kurukshetra University Kurukshetra- 136119 (Haryana), India under the guidance of Professor **Dinesh Kumar Aggarwal (PhD Cantab, UK & ISCA Homi J Bhabha Scientist Awardee) Vice-Chancellor** , YMCA University of Science & Technology , Faridabad (Haryana)- India.

**UGC-NET Qualified ( June 2015 & Dec 2015)**

**M.Tech (ECE)** *(VLSI Design and Embedded systems)*
Passed from Dep't. of Electronics and Communication Engg.
Guru Jambheswar University of Science & Technology
Hisar (HR) **with 78.16% marks, WITH DISTINCTION.**

**1st Rank in PhD Merit List @ Kurukshetra University.**

## PROFESSIONAL EXPERIENCE:

- Presently Research Scholar & Asstt. Prof. Electronics Department Kurukshetra University (Haryana), India.

## TECHNICAL SKILLS

√ Window-xp and Linux operating systems
√ Mentor Graphics (SDL, LVS, PEX)
√ Tanner EDA ( L-Edit, S-Edit, T-spice)
√ Mat lab 7.8
√ Model-sim 6.0 (VHDL modeling)
√ Ed-Win xp (Circuit Simulation)
√ Multi- Sim (for circuit simulation)
√ Xilinx (for digital design automation)
√ Microprocessor Programming (8085 & 8086)
√ Active- HDL 7.2 (for VHDL/ Verilog modeling & Simulation)
√ Silvaco's ATHENA, ATLAS, DEV-EDIT
√ MEDEA

## PROGRAMMING LANGUAGES

**Software Language:**

√ C

**Hardware Languages:**

√ VHDL

√ Verilog HDL

## RESEARCH PUBLICATIONS (8):

**International Conferences (5):**

1. A paper entitled *"Design and implementation of CMOS Voltage Reference for High Performance and Low Power",* published & presented in **International Conference on Emerging & Futuristic System and Technology (ICE-FST'09), (April 09-11, 2009)** organized by Laxmi Devi Institute of Engineering and Technology (LIET) Alwar, (Rajasthan)- India.

2. A paper entitled *"Design and Analysis of a Low Power CMOS Voltage Reference"* published & presented in **International Conference on Innovative Technologies (ICIT-09), (June 18-19, 2009)** organized by PDM college of Engineering Bahadurgarh, Haryana-124507, in collaboration with IEEE-IMS / EMBS (Delhi)- India.

3. A paper entitled *"A Sub-threshold Operation Based Low Power CMOS Voltage Reference"* published & presented in **International Conference on Innovative Technologies (ICIT-09), (June 18-19, 2009)** organized by PDM college of Engineering Bahadurgarh, Haryana-124507, in collaboration with IEEE-IMS / EMBS (Delhi)- India.

4. A paper entitled *"On-chip Interconnects for Low Power VLSI applications"* published & presented in **proceeding of International Conference on Computer Applications in Manufacturing and Food Technologies and Bio-Nano Engineering (CAMBTE-2014)** (February 21-22, 2014) organized by Universal Institute of Technology, NH-10, 12 km Stone, Garhi, Hansi, Hisar (Haryana)- India. ISBN No. 978-93-81191.

5. A paper entitled *"Embedded System for Real Time Applications in Military and Aerospace"* published & presented in **proceeding of International Conference on Computer Applications in Manufacturing and Food Technologies and Bio-Nano Engineering (CAMBTE-2014)** (February 21-22, 2014) organized by Universal Institute of Technology, NH-10, 12 km Stone, Garhi, Hansi, Hisar (Haryana)- India. ISBN No. 978-93-81191.

**National Conferences (3):**

1. A paper entitled **"Analysis and Implementation of CMOS current mirror for low voltage and low power"** published & presented in national conference **(July 18-19,2009)** organized by Adesh Insitute of Engg. & Technology, Sadiq Road, Faridkot (PUNJAB).

2. A paper entitled **"Embedded system/RTOS:An Overview"** presented **in ETECE-11(April 22-23, 2011)** organized by Panipat Insitute of Engg. &

Technology, Samalkha Panipat (Haryana).

3. A paper entitled **"3-D VLSI Circuit Integration with TSV"** presented in national conference **RTET-12 (June 04-06, 2012)** organized by PPIMT-Hisar (Haryana).

## EXTRA CURRICULAR ACTIVITIES AND ACHIEVEMENTS

- To do something new.
- Attended & active member of the IMS **(Indian Microelectronic Society)** Conference held at Kurukshetra University Kurukshetra-136119 (Haryana).
- Attended & participated in the Workshop on **"Contemporary Communication Technologies**" held at Guru Jambheswar University of Science & Technology, Hisar-125001 (Haryana).
- Attended national seminar on **"Atom for peace"** at Guru Jambheswar University of Science & Technology, Hisar-125001 (Haryana).
- Attended & participated in the Workshop on **"Embedded system-A design approach"** organized by Gurgaon Institute of Technology & Management (GITM) Gurgaon (Haryana).
- Attended & participated in national seminar on **"Industrial Motivation Campion"** organized MSMEDI by Govt. of India, held at R.P.S.College of Engg. & Technology Mohindergarh-123029 (Haryana).
- Attended and participated in AICTE sponsored National level Staff Development Program on **"Smart Materials"** organized by SDITM-Panipat (Haryana).
- Co-Editor of the book titled " **Vistas in Computer Aided Agri-Bio and Engineering Technologies" ISBN No.:** 978-93-81191.