Curriculum vitae

RAKESH KUMAR

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CAREER OBJECTIVE

- Being a dynamic and disciplined person. To be a part of a reputed organization, work in a healthy and challenging environment and create an equivalent working condition for others.

TECHNICAL QUALIFICATION

- M.Tech. in **"Electronics and Communication Engineering"** from N.C.College of Engineering, Panipat, Affiliated to Kurukshetra University, Kurukshetra in 2007.
- M.Sc. in **"Electronic Science"** with **"Emphasis in VLSI Design"** from Electronic Science Department, Kurukshetra University, Kurukshetra in 2004.
- Bachelor Degree in Electronic Science from A.I.J.H.M.College Rohtak Affiliated to Maharshi Dayanand University, Rohtak in 2002.

ACADEMIC QUALIFICATION

- XIIth from A.I.J.H.M.College Rohtak Affiliated to H.B.S.E in 1999.
- Xth from C.R.Memorial Public School, Rohtak Affiliated to C.B.S.E in 1997.

TEACHING EXPERIENCE (EIGHT YEARS)

- Working as a Assistant Professor in Gurgaon Institute of Technology and Management (GITM), Gurgaon from 1st Feb-2011 to till date.
- Worked as a Senior Lecturer in Gurgaon Institute of Technology and Management (GITM), Gurgaon from 1st Sep-2008 to 31st Jan-2011.
- Worked as a Lecturer in Gurgaon Institute of Technology and Management (GITM), Gurgaon from 28th May-2007 to 30th Aug-2008.
- Worked with **IICE Technologies Ltd., New Delhi** as Faculty Cum Project Guide from August-2004 to July-2005.

COMPUTER KNOWLEDGE

- Executive Quick Information Technology Program from Software Technology Group, New Delhi in 2001.
- Web Design-Internet, HTML, Tool and Java Script from Software Technology Group, New Delhi in 2001.

TECHNICAL CAPABILITIES

- **Language Known**

  Hardware Description Language: VHSIC HDL, VERILOG HDL

- **EDA Tools:**

  Leonardo Spectrum, Xilinx ISE 6.3i, Model Sim XE II 5.8c, Blue PC, Altera MAX PLUS II Baseline, Active-HDL 6.3, Alliance for VHDL, Electronic Workbench. TINA software.

- **FPGA Implementation:** Spartan 2 Xilinx FPGA.

- **Fabrication:**

  Familiarization with Device Fabrication Techniques (Wafer Cleaning, Oxidation, Metallization, Photolithography) also with Vacuum System.

TRAINING PROJECT (M.TECH.)

Title: "Basic Telephony and Switch Operations (Switch - Siemens EWSD)"

Duration: Six Month.

Place of Work: Bharti Infotel Limited-Basic Services North (Main Switching Unit (MSU)-Gurgaon).

Under the Guidance: Mr. Devinder Singh (Manager-Switch)

ACHIEVEMENTS

- **"Appreciation Award"** in recognition of outstanding & dedicated services rendered for the upliftment of society from Rotary Club, Gurgaon.
- **"Certificate of Merit"** in NSS from M.D.U. in 2001-2002.
- Participated in **"Poster Competition"** in NASET-2004.
- 3rd Position in Inter College **"Boxing Championship"** of MDU Rohtak in 1999-2000.
- First prize in **"Electronic Mela Exhibition"** held in A.I.J.H.M.College, Rohtak in Nov. 2001.
- **"Best Student"** Award of the Electronic Science Department, Kurukshetra University, Kurukshetra.
- **"Certificate of Appreciation"** of **"Junior Secretary"** in **N**ational **L**evel **S**ymposium on **E**lectronic **T**echnology **(NASET)** 2002-03.

- **"Certificate of Appreciation"** of **"Secretary"** in **N**ational **L**evel **S**ymposium on **E**lectronic **T**echnology **(NASET)** 2003-04.

SEMINARS / CONFERENCES DELIVERED AND ATTENDED

- Attended "Certified Ethical Hacking Expert (Level1) workshop at GITM, Gurgaon on 11-12th Sep, 2010 and Co-ordinate the Hack Track workshop.
- Attended a National Seminar on "LASER & Optoelectronics Technologies (LOT-2010)" held on 8th April, 2010 in GCE, Gurgaon.
- Attended a National Workshop on "Embedded System-A Design Approach" held on 26-27 Feb, 2010 in GITM, Gurgaon.
- Attended winter school on VHDL for Digital Hardware Designers-VDHD 09 in National Institute of Technology (NIT), Kurukshetra
- Seminar delivered on **"Mobile Communication"** in Faculty Development Program (FDP) GITM, Gurgaon.
- Attended national brain storming Seminar on **"Thin Film Transistor (TFT) Technology**" in Amity University.
- Attended **IMS (Indian Microelectronics Society)** Conference-2006 in Kurukshetra University, Kurukshetra.

PAPER PUBLISHED IN NATIONAL / INTERNATIONAL CONFERENCES, NATIONAL / INTERNATIONAL JOURNAL

1. Rakesh Kumar, "Provide better QoS using fuzzy and neural approach when congestion control in the networks" in NCRTEMS 24th- 25th, February-2011, ACME Palwal (Haryana).

2. Rakesh Kumar,"Mobile Ad-hoc network, routing protocols and test bed implementation in National Seminar on Advances in Computing (AIC) 30th April & 1st May-2011,DCTM Palwal (Haryana).

3. Poonam Tyagi ,Ravindra Prakash Gupta,Rakesh Kumar Gill, "Comparative analysis of Cluster Based Routing Protocols Used in Heterogeneous Wireless Sensor Network ",International Journal Of Soft Computing And Engineering (IJSCE), ISSN: 2231-2307,Volume No-1, Issue No -5, November-2011

4. Rakesh Kumar, "Design & VLSI Implementation of TDM controller" in National Seminar on NEIET 28th April-2012, BITS Bhiwani (Haryana)

5. Rakesh Kumar, "Performance analysis of OFDM transceiver using FEC Technique" in International Conference on ETEM 23th – 24th , June-2012, SPIT Rohtak (Haryana).

6. Rakesh Kumar, "An Improved Self Cancellation Scheme to Reduce Non-Linearity in OFDM Spectrum", International Journal of Computational Engineering Research (IJCER), ISSN-2250-3005, Volume No-2, Issue No -4, July-August 2012.

7. Bijender Kumar, Rakesh Kumar, "Protection of Heart Attack using ANN", National Conference on Emerging Trends in Science, Technology & Management-Vision 2020 on 18th -19th, January-2013, GITM Gurgaon (Haryana).

8. Rakesh Kumar. "Overview of Security in VANET" International Journal of Engineering, Applied & Management Sciences Paradigms (IJEAM), ISSN:2320-6608, Volume No-04, Issue No.-01, 01 June-2013.

9. Rakesh Kumar, "Sybil Attack Detection and Prevention using AODV in VANET" International Journal of Computer Science and Management Studies (IJCSMS), ISSN: 2231-5268, Volume No.-13, Issue No.-07, 07 September-2013.